## ND.448 0003 4084337460 LSI LOGIC → 915712732885 10/17/2006 11:42 PART B - FEE(S) TRANSMITTAL Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 OCT 17 2006 Alexandria, Virginia 22313-1450 or Fax (571)-273-2885 INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks I through 5 should be completed where appropriate for further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicating diess corrected below or directed otherwise in Block I, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for EMMINISTED for notifications. Note: A certificate of mailing can only be used for domestic mailings of the Fec(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission. CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) 08/17/2006 024319 750n Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2835, on the date indicated below. LSI LOGIC CORPORATION 1621 BARBER LANE MS: D-106 MILPITAS, CA 95035 Consitor's name 10/18/2006 RMEBRAH1 00000089 122252 10659134 (Signature 01 FC:1501 1400.00 DA (Dzte $\Rightarrow \circ \circ \subset$ 300.00 DA 02 FC:1504 ATTORNEY DOCKET NO. CONFIRMATION NO. FIRST NAMED INVENTOR APPLICATION NO. FILING DATE 4827 09/10/2003 Mohammad R. Mirabedini 10/659,134 TITLE OF INVENTION: APPARATUS AND METHOD OF MANUFACTURE FOR INTEGRATED CIRCUIT AND CMOS DEVICE INCLUDING EPITAXIALLY GROWN DIELECTRIC ON SILICON CARBIDE DATE DUE ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE SMALL ENTITY APPLN TYPE \$1700 11/17/2006 \$1400 \$300 80 nonprovisional NO EXAMINER ART UNIT CLASS-SUBCLASS COLEMAN, WILLIAM D 2823 438-105000 Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list 1 L. Jon Lindsay (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. "Fcc Address" indication (or "Fcc Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (B) RESIDENCE: (CITY and STATE OR COUNTRY) (A) NAME OF ASSIGNEE Milpitas, California, USA LSI Logic Corporation Please check the appropriate assignce category or categories (will not be printed on the patent): 🔲 Individual 🎞 Corporation or other private group entity 🚨 Government 4b, Payment of Fcc(s): (Please first reapply any previously paid issue fee shown above) 4a. The following fee(s) are submitted: 🛚 Issue Fee A check is enclosed. ☑ Publication Fee (No small entity discount permitted)

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